



R.M.K COLLEGE OF ENGINEERING AND TECHNOLOGY

R.S.M Nagar, Pudukkottai-601 206



DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

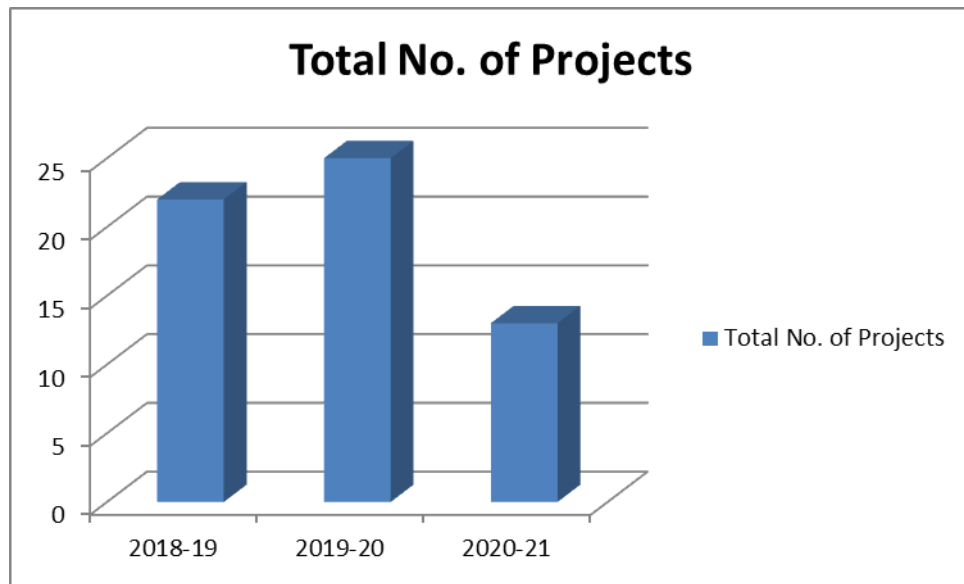
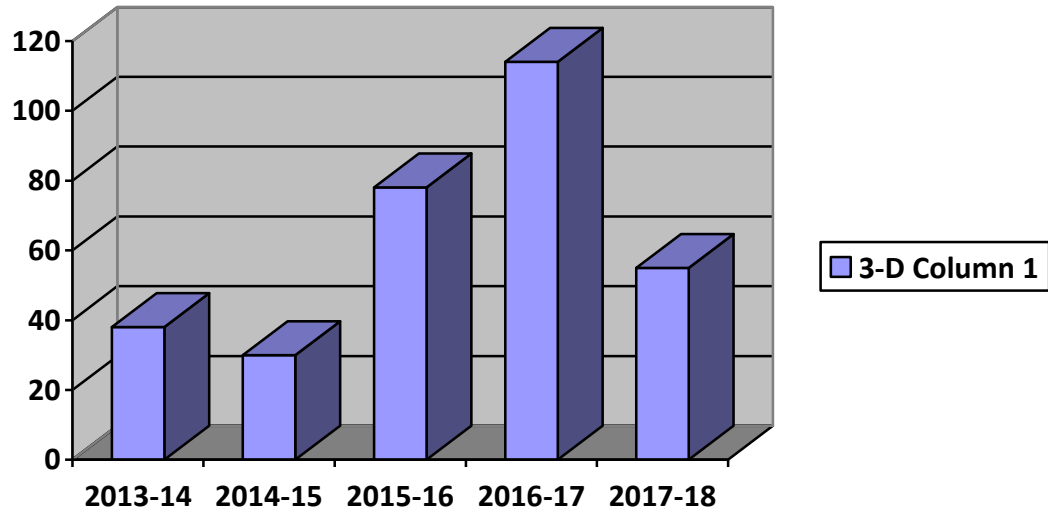
MINI PROJECT

S.No.	Academic Year	Department	No of Students Participated in Mini project Contest				No of Projects				Total Projects
			Odd		Even		Odd		Even		
			Internal	External	Internal	External	Internal	External	Internal	External	
1	2013-2014	ECE	-	-	80	-	26	-	-	-	26
2	2014-2015	ECE	121	-	-	-	38	-	-	-	38
3	2015-2016	ECE	64	11	173	-	19	11	48		78
4	2016-2017	ECE	248	-	87	31	68	-	23	13	114
5	2017-2018	ECE	128	-	52	-	41	-	14	-	55
6	2018-2019	ECE	71	-	-	-	22	-	-	-	22
7	2019-2020	ECE	82	-	-	-	25	-	-	-	25
8	2020-2021	ECE	-	-	46	-	-	-	13	-	13

Mini Project

Miniproject

S.NO	Academic Year	Title of the Event	Date of the event	Sponsors	No.of Projects Contributed by ECE Students
1.	2013 - 2014	Intra department Miniproject Contest	07.03.14	-	38
2.	2014 - 2015	Intra department Miniproject Contest	05.09.2014	-	30
3.	2015 - 2016	Miniproject Contest	31.09.2015	IET	30
4.		Intra department Miniproject Contest	30.3.2016	-	48
5.	2016 - 2017	Intradepartment Miniproject Contest	30.07.2016	-	68
6.		National Level Project Competition(RACKET-2017)	27.2.2017	-	36
7.	2017 - 2018	Intra department Miniproject Contest	20.9.2017	-	41
8.		Intra department Miniproject Contest	03.03.18	-	14
9.	2018 - 2019	Intra department Miniproject Contest	22.9.18	-	22
10.	2019 - 2020	Intra department Miniproject Contest	30.8.19	-	25
11.	2020 - 2021	Intra department Miniproject Online Contest	07.06.21	-	13





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Lab Utilisation for Miniproject

S.N O	NAME OF THE LAB	EVEN SEM 2015 -16	ODD SEM 2015 -16	Total No. Projec ts 2015- 16	EVEN SEM 2016 -17	ODD SEM 2016-17	Total No. Projec ts 2016- 17	EVEN SEM 2017- 18	ODD SEM 2017 -18	Total No. Projec ts 2017- 18	ODD SEM 2018- 19	ODD SEM 2019- 20	EVEN SEM 2020- 21
1.	MICROPROCES SOR AND MICROCONTR OLLER LABORATORY	09	05	14	03	09	12	05	09	14	6	7	Online
2.	ELECTRONICS/ COMMUNICAT ION LABORATORY	09	05	14	08	08	16	04	08	12	2	4	
3.	DIGITAL AND LIC LABORATORY	16	06	22	03	12	15	02	05	07	11	10	
4.	VLSI DESIGN/EMBE DDED LABORATORY	09	02	11	05	07	12	04	11	15	3	4	

